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E. Willis  
11-16-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

M. TODA et al

Serial No.

Filed: May 5, 2000

For: DATA PROCESSOR AND DATA PROCESSING SYSTEM

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Prior to the examination thereof, please amend the  
above-identified application as follows.

IN THE CLAIMS

Please amend claims 4, 5, 6, 10, 11, 12, 17 and add  
new claims 18-39 as follows.

A1 Sub  
B1

4. (Amended) A data processor according to claim[s]  
2 [or 3], wherein said CPU, said address translation means,  
said address output means, and said PCMCIA interface in  
said external device are formed on the same semiconductor  
substrate.